

ISSUE CLASSIFICATION	
Class	Subclass

N

O.I.P.E.		PATENT DATE
AS		
SCANNED	Q.A.	

APPLICATION NO. 09/922755	CONT/PRIOR	CLASS 714	SUBCLASS 31	ART UNIT 2184	EXAMINER B. D. L. YAN
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Christopher Giles

RSO

V 34
x 41

Multiprocessor system and method for simultaneously placing all processors into debug mode

PTO-2040
- 12/89

ISSUING CLASSIFICATION															
ORIGINAL				CROSS REFERENCE(S)											
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)										
INTERNATIONAL CLASSIFICATION															

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
	_____ (Date)			
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____	_____ (Primary Examiner)		ISSUE FEE	
	_____ (Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		ISSUE BATCH NUMBER	
		_____ (Date)		
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